

FAST - [datacapturememorybypass.wsp:1]

File View Edit Tools Window Help

(14188) (cpu or processor) near2 interface
(35585) memory near2 (interface or controller)
(1189) ((cpu or processor) near2 interface) with (buffer\$1 or fifo\$2)
(4954) (memory near2 (interface or controller)) with (buffer\$1 or fifo\$2)
(157) (((cpu or processor) near2 interface) with (buffer\$1 or fifo\$2)) same ((memory near
(126594) (separat\$3 or dedicat\$3 or bypass\$3) near2 (path\$1 or line\$1 or channel\$1 or l
(47) (((cpu or processor) near2 interface) with (buffer\$1 or fifo\$2)) same ((memory near
(6355) data near5 capture
(9292) strobe adj signal
(0) (((((cpu or processor) near2 interface) with (buffer\$1 or fifo\$2)) same ((memory near
(0) (((((cpu or processor) near2 interface) with (buffer\$1 or fifo\$2)) same ((memory near
(3) ((memory near2 (interface or controller)) with (buffer\$1 or fifo\$2)) and ((data near5.
(23) (data near5 capture) with (strobe adj signal)
(9) (((((cpu or processor) near2 interface) with (buffer\$1 or fifo\$2)) same ((memory near
(2) (((((cpu or processor) near2 interface) with (buffer\$1 or fifo\$2)) same ((memory near
(1105) asynchronous with delay
(0) (asynchronous with delay) and (((((cpu or processor) near2 interface) with (buffer\$1 or
(0) (asynchronous with delay) and (((((cpu or processor) near2 interface) with (buffer\$1 or
(0) ((data near5 capture) with (strobe adj signal)) and (asynchronous with delay)
(29) ((memory near2 (interface or controller)) with (buffer\$1 or fifo\$2)) and (asynchrono
(12861) propagation near5 delay
(47753) (path\$1 or line\$1 or length\$1 or trace\$1) near5 delay
(3325) (propagation near5 delay) same ((path\$1 or line\$1 or length\$1 or trace\$1) near5 d
(109) (asynchronous with delay) and ((propagation near5 delay) same ((path\$1 or line\$1 or len...
(0) (((((cpu or processor) near2 interface) with (buffer\$1 or fifo\$2)) same ((memory near

Introduc\$3 with (propagation near5 delay) same (short\$3 with ((path\$1 or line\$1 or length\$1 or trace\$1) near5 delay))

	U	1	Document ID	Issue Dat	Page	Title	Current OR	Current XR	Retrieval	Inventor
1			US 6421784	20020716	16	Programmable delay circuit	713/401	327/276		Chu, Albert Manhee
2			US 6069490	20000530	14	Routing architecture using a	326/41	326/39		Ochotta, Emil S. et al.
3			US 5801546	19980901	39	Interconnect architecture	326/39	326/41		Pierce, Kerry M. et al.
4			US 5760604	19980602	39	Interconnect architecture	326/41	326/39;		Pierce, Kerry M. et al.
5			US 5581199	19961203	40	Interconnect architecture	326/41	326/39		Pierce, Kerry M. et al.
6			US 5386478	19950131	7	Sound system remote	381/103			Plunkett, Bradley J.
7			US 4951269	19900821	31	Echo canceller with short	367/135	367/901;		Amano, Fumio et al.

Start EAST - [data... 8:20 AM

FAST - [datacapturememorybypass.wsp:1]

File View Edit Tools Window Help

D [Icons]

(1105) asynchronous with delay
(0) ((asynchronous with delay) and (((cpu or processor) near2 interface) with (buffer\$1 or
(0) ((asynchronous with delay) and (((cpu or processor) near2 interface) with (buffer\$1 or
(0) ((data near5 capture) with (strobe adj signal)) and (asynchronous with delay)
(29) ((memory near2 (interface or controller)) with (buffer\$1 or fifo\$2)) and (asynchronous
(12861) propagation near5 delay
(47753) (path\$1 or line\$1 or length\$1 or trace\$1) near5 delay
(3325) (propagation near5 delay) same ((path\$1 or line\$1 or length\$1 or trace\$1) near5 d
(109) (asynchronous with delay) and ((propagation near5 delay) same ((path\$1 or line\$1 c
(0) (((cpu or processor) near2 interface) with (buffer\$1 or fifo\$2)) same ((memory near
(359) (((cpu or processor) near2 interface) with (buffer\$1 or fifo\$2)) and ((memory near
(0) ((asynchronous with delay) and ((propagation near5 delay) same ((path\$1 or line\$1 or
(0) ((asynchronous with delay) and ((propagation near5 delay) same ((path\$1 or line\$1 or
(20) ((asynchronous with delay) and ((propagation near5 delay) same ((path\$1 or line\$1 o
(2) 5859810.URPN.
(2) 5859810.URPN.
(608) introduc\$3 with (propagation near5 delay)
(330) (introduc\$3 with (propagation near5 delay)) and ((path\$1 or line\$1 or length\$1 or trace...
(128) (introduc\$3 with (propagation near5 delay)) with ((path\$1 or line\$1 or length\$1 or tra
(3) ((introduc\$3 with (propagation near5 delay)) with ((path\$1 or line\$1 or length\$1 or trac
(2857) short\$3 with ((path\$1 or line\$1 or length\$1 or trace\$1) near5 delay)
(871) short\$3 with (propagation near5 delay)
(18) (short\$3 with (propagation near5 delay)) same "38"
(162) (short\$3 with (propagation near5 delay)) same (short\$3 with ((path\$1 or line\$1 or lei
(131) (introduc\$3 with (propagation near5 delay)) same (short\$3 with ((path\$1 or line\$1 or lei

(Introduc\$3 with (propagation near5 delay)) same (short\$3 with ((path\$1 or line\$1 or length\$1 or trace\$1) near5 delay))

DBs: USPAT ☐ Pursa
Default operator: OR ☒ Highlight all hit terms initially

Search Results

	U	1	Document ID	Issue Dat	Page	Title	Current OR	Current XR	Retrieval	Inventor
1	<input type="checkbox"/>	<input type="checkbox"/>	US 6421784	20020716	16	Programmable delay circuit	713/401	327/276		Chu, Albert Manhee
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Start [Icons] EAST - [data...]

8:19 AM